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Proceedings of the 8th International Conference on VLSI Design, January 4-7, 1995, New Delhi, India - 1995
Presents papers from the January 1995 conference. Topics include routing, hardware-software design/CAD, sequential automatic test pattern generation, logic synthesis, VLSI arithmetic, and chip design. Includes tools and technology poster sessions, and a panel discussion on India's role in the VLSI w
Logic Synthesis and Verification - Soha Hassoun 2012-12-06
Research and development of logic synthesis and verification have matured considerably over the past two decades. Many commercial products are available, and they have been critical in harnessing advances in fabrication technology to produce today's plethora of electronic components. While this maturity is assuring, the advances in fabrication continue to seemingly present unwieldy challenges. Logic Synthesis and Verification provides a state-of-the-art view of logic synthesis and verification. It consists of fifteen chapters, each focusing on a distinct aspect. Each chapter presents key developments, outlines future challenges, and lists essential references. Two unique features of this book are technical strength and comprehensiveness. The book chapters are written by twenty-eight recognized leaders in the field and reviewed by equally qualified experts. The topics collectively span the field. Logic Synthesis and Verification fills a current gap in the existing CAD literature. Each chapter contains essential information to study a topic at a great depth, and to understand further developments in the field. The book is intended for seniors, graduate students, researchers, and developers of related Computer-Aided Design (CAD) tools. From the foreword: "The commercial success of logic synthesis and verification is due in large part to the ideas of many of the authors of this book. Their innovative work contributed to design automation tools that permanently changed the course of electronic design." by Aart J. de Geus, Chairman and CEO, Synopsys, Inc.
1993 Symposium on VLSI Circuits - 1993

Developing Expert CAD Systems - V. Begg 2012-12-06
The importance of CAD to electronics technology Computer-aided design (CAD) is one way of coping with the problem of how to design and build very complex systems. This problem is particularly acute in electronics technology. Designs are now (1984) said to be design-limited, rather than technology-limited. It can take months to generate a design for a chip, so that it might be obsolete before it can be manufactured. Manual design of large-scale integration (LSI) chips (circa 10,000 gates) is almost impossible. However, using current technology it is possible to produce chips having 250,000 gates. It is understandable, therefore, that there is great interest in improving existing CAD systems. Designers of CAD systems are concerned with formalizing and automating as

much of the design task as possible. Automating design of any kind has long been acknowledged as a project fraught with intractable problems. A human designer has to have an understanding of the nature of the materials used in manufacture, a knowledge of common problems and well-tried solutions, and above all, creativity in producing new designs. Understanding, knowledge and creativity are three properties even the most artificially intelligent of computer programs have been entirely lacking in until very recently. Some people would deny computers these qualities entirely, on philosophical grounds (eg Dreyfus 1979; Searle 1981). There are few theories in cognitive psychology which can help.

Computer, Communication and Electrical Technology - Debatosh Guha 2017-03-16

The First International Conference on Advancement of Computer, Communication and Electrical Technology focuses on key technologies and recent progress in computer vision, information technology applications, VLSI, signal processing, power electronics & drives, and application of sensors & transducers, etc. Topics in this conference include: Computer Science This conference encompassed relevant topics in computer science such as computer vision & intelligent system, networking theory, and application of information technology. Communication Engineering To enhance the theory & technology of communication engineering, ACCET 2016 highlighted the state-of-the-art research work in the field of VLSI, optical communication, and signal processing of various data formatting. Research work in the field of microwave engineering, cognitive radio and networks are also included. Electrical Technology The state-of-the-art research topic in the field of electrical & instrumentation engineering is included in this conference such as power system stability & protection, non-conventional energy resources, electrical drives, and biomedical engineering. Research work in the area of optimization and application in control, measurement & instrumentation are included as well.

Scientific and Technical Aerospace Reports - 1992

APPSC-Andhra Pradesh Assistant Engineer-AE-Mechanical Exam Ebook-PDF - Chandresh Agrawal 2022-03-14

SGN. The Ebook-PDF APPSC-Andhra Pradesh Assistant Engineer-AE-Mechanical Exam Covers Objective Questions From Various Previous Years' Papers With Answers Plus Mechanical Engineering Chapters.

VLSI Circuits and Systems - 2005

Algorithms for VLSI Physical Design Automation - Naveed A. Sherwani 2012-12-06

Algorithms for VLSI Physical Design Automation, Second Edition is a core reference text for graduate students and CAD professionals. Based on the very successful First Edition, it provides a comprehensive treatment of the principles and algorithms of VLSI physical design, presenting the concepts and algorithms in an intuitive manner. Each chapter contains 3-4 algorithms that are discussed in detail. Additional algorithms are presented in a somewhat shorter format. References to advanced algorithms are presented at the end of each chapter. Algorithms for VLSI Physical Design Automation covers all aspects of physical design. In 1992, when the First Edition was published, the largest available microprocessor had one million transistors and was fabricated using three metal layers. Now we process with six metal layers, fabricating 15 million transistors on a chip. Designs are moving to the 500-700 MHz frequency goal. These stunning developments have significantly altered the VLSI field: over-the-cell routing and early floorplanning have come to occupy a central place in the physical design flow. This Second Edition introduces a realistic picture to the reader, exposing the concerns facing the VLSI industry, while maintaining the theoretical flavor of the First Edition. New material has been added to all chapters, new sections have been added to most chapters, and a few chapters have been completely rewritten. The textual material is supplemented and clarified by many helpful figures. Audience: An invaluable reference for professionals in layout, design automation and physical design.

Circuit Theory and Design - R. Boite 1981

Conference Record, Fifteenth Asilomar Conference on Circuits, Systems & Computers - Shu-Park Chan 1981

VLSI for Neural Networks and Artificial Intelligence - Jose G. Delgado-Frias 2013-06-29
Neural network and artificial intelligence algorithrns and computing have increased not only in complexity but also in the number of applications. This in turn has posed a tremendous need for a larger computational power that conventional scalar processors may not be able to deliver efficiently. These processors are oriented towards numeric and data manipulations. Due to the neurocomputing requirements (such as non-programming and learning) and the artificial intelligence requirements (such as symbolic manipulation and knowledge representation) a different set of constraints and demands are imposed on the computer architectures/organizations for these applications. Research and development of new computer architectures and VLSI circuits for neural networks and artificial intelligence have been increased in order to meet the new performance requirements. This book presents novel approaches and trends on VLSI implementations of machines for these applications. Papers have been drawn from a number of research communities; the subjects span analog and digital VLSI design, computer design, computer architectures, neurocomputing and artificial intelligence techniques. This book has been organized into four subject areas that cover the two major categories of this book; the areas are: analog circuits for neural networks, digital implementations of neural networks, neural networks on multiprocessor systems and applications, and VLSI machines for artificial intelligence. The topics that are covered in each area are briefly introduced below.
Digest of Technical Papers - 1999

The First Outstanding 50 Years of "Università Politecnica delle Marche" - Sauro Longhi 2019-12-16
The book describes the significant multidisciplinary research findings at the Università Politecnica delle Marche and the expected future advances. It addresses some of the most dramatic challenges posed by today's fast-growing, global society and the changes it has caused. It also discusses solutions to improve the wellbeing of human beings. The book covers the main research achievements in the different disciplines of the physical sciences and engineering, as well as several research lines developed at the university's Faculty of Engineering in the fields of electronic and information engineering, telecommunications, biomedical engineering, mechanical engineering, manufacturing technologies, energy, advanced materials, chemistry, physics of matter, mathematical sciences, geotechnical engineering, circular economy, urban planning, construction engineering, infrastructures and environment protection, technologies and digitization of the built environment and cultural heritage. It highlights the international relevance and multidisciplinarity of research at the university as well as the planned research lines for the next years.

VLSI CAD Tools and Applications - Wolfgang Fichtner 2012-12-06
The summer school on VLSf GAD Tools and Applications was held from July 21 through August 1, 1986 at Beatenberg in the beautiful Bernese Oberland in Switzerland. The meeting was given under the auspices of IFIP WG 10. 6 VLSI, and it was sponsored by the Swiss Federal Institute of Technology Zurich, Switzerland. Eighty-one professionals were invited to participate in the summer school, including 18 lecturers. The 81 participants came from the following countries: Australia (1), Denmark (1), Federal Republic of Germany (12), France (3), Italy (4), Norway (1), South Korea (1), Sweden (5), United Kingdom (1), United States of America (13), and Switzerland (39). Our goal in the planning for the summer school was to introduce the audience into the realities of CAD tools and their applications to VLSI design. This book contains articles by all 18 invited speakers that lectured at the summer school. The reader should realize that it was not intended to publish a textbook. However, the chapters in this book are more or less self-contained treatments of the particular subjects. Chapters 1 and 2 give a broad introduction to VLSI Design. Simulation tools and their algorithmic foundations are treated in Chapters 3 to 5 and 17. Chapters 6 to 9 provide an excellent treatment of modern layout tools. The use of CAD tools and trends in the design of 32-bit microprocessors are the topics of Chapters 10 through 16. Important aspects in VLSI testing and testing strategies

are given in Chapters 18 and 19.
Design systems for VLSI circuits - Giovanni DeMicheli 1987-07-31
Proceedings of the NATO Advanced Study Institute, L'Aquila, Italy, July 7-18, 1986
Design Methodologies for VLSI Circuits - NATO Advanced Study Institute on "Design Methodologies for VLSI Circuits" 1982-02-28

Integrated Electronics - 2018

Binary Decision Diagrams and Applications for VLSI CAD - Shin-ichi Minato 2012-12-06
Symbolic Boolean manipulation using binary decision diagrams (BDDs) has been successfully applied to a wide variety of tasks, particularly in very large scale integration (VLSI) computer-aided design (CAD). The concept of decision graphs as an abstract representation of Boolean functions dates back to the early work by Lee and Akers. In the last ten years, BDDs have found widespread use as a concrete data structure for symbolic Boolean manipulation. With BDDs, functions can be constructed, manipulated, and compared by simple and efficient graph algorithms. Since Boolean functions can represent not just digital circuit functions, but also such mathematical domains as sets and relations, a wide variety of CAD problems can be solved using BDDs. `Binary Decision Diagrams and Applications for VLSI CAD provides valuable information for both those who are new to BDDs as well as to long time aficionados.' -from the Foreword by Randal E. Bryant. `Over the past ten years ... BDDs have attracted the attention of many researchers because of their suitability for representing Boolean functions. They are now widely used in many practical VLSI CAD systems. ... this book can serve as an introduction to BDD techniques and ... it presents several new ideas on BDDs and their applications. ... many computer scientists and engineers will be interested in this book since Boolean function manipulation is a fundamental technique not only in digital system design but also in exploring various problems in computer science.' - from the Preface by Shin-ichi Minato.
Advances in Computers - 1997-08-19
Since its first volume in 1960, Advances in Computers has presented detailed coverage of innovations in hardware and software and in computer theory, design, and applications. It has also provided contributorswith a medium in which they can examine their subjects in greater depth and breadth than that allowed by standard journal articles. As a result, many articles have become standard references that continue to be of significant, lasting value despite the rapid growth taking place in the field.
VLSI Design and Test - S. Rajaram 2019-01-24
This book constitutes the refereed proceedings of the 22st International Symposium on VLSI Design and Test, VDAT 2018, held in Madurai, India, in June 2018. The 39 full papers and 11 short papers presented together with 8 poster papers were carefully reviewed and selected from 231 submissions. The papers are organized in topical sections named: digital design; analog and mixed signal design; hardware security; micro bio-fluidics; VLSI testing; analog circuits and devices; network-on-chip; memory; quantum computing and NoC; sensors and interfaces.
Digital VLSI Systems - Mohamed I. Elmasry 1985

Low Power VLSI Design and Technology -

Asynchronous Circuit Design for VLSI Signal Processing - Teresa H. Meng 2011-06-27
Asynchronous Circuit Design for VLSI Signal Processing is a collection of research papers on recent advances in the area of specification, design and analysis of asynchronous circuits and systems. This interest in designing digital computing systems without a global clock is prompted by the ever growing difficulty in adopting global synchronization as the only efficient means to system timing. Asynchronous circuits and systems have long held interest for circuit designers and researchers alike because of the inherent challenge involved in designing these circuits, as well as developing design techniques for them. The frontier research in this area can be traced back to Huffman's publications `The Synthesis of Sequential Switching Circuits' in 1954 followed by Unger's book, `Asynchronous Sequential Switching Circuits' in 1969 where a theoretical foundation for handling logic hazards was established. In the last few years a growing number of

researchers have joined force in unveiling the mystery of designing correct asynchronous circuits, and better yet, have produced several alternatives in automatic synthesis and verification of such circuits. This collection of research papers represents a balanced view of current research efforts in the design, synthesis and verification of asynchronous systems.
International Conference on VLSI and CAD. - 1999

Digital Libraries and Multimedia - Bharat Bhargava 2007-05-08
Digital Libraries and Multimedia brings together in one place important contributions and up-to-date research results in this fast moving area. Digital Libraries and Multimedia serves as an excellent reference, providing insight into some of the most challenging research issues in the field.

Computational Logic — CL 2000 - John Lloyd 2003-06-26
These are the proceedings of the First International Conference on Compu- tional Logic (CL 2000) which was held at Imperial College in London from 24th to 28th July, 2000. The theme of the conference covered all aspects of the theory, implementation, and application of computational logic, where computational logic is to be understood broadly as the use of logic in computer science. The conference was collocated with the following events: { 6th International Conference on Rules and Objects in Databases (DOOD 2000) { 10th International Workshop on Logic-based Program Synthesis and Tra- formation (LOPSTR 2000) { 10th International Conference on Inductive Logic Programming (ILP 2000). CL 2000 consisted of seven streams: { Program Development (LOPSTR 2000) { Logic Programming: Theory and Extensions { Constraints { Automated Deduction: Putting Theory into Practice { Knowledge Representation and Non-monotonic Reasoning { Database Systems (DOOD 2000) { Logic Programming: Implementations and Applications. The LOPSTR 2000 workshop constituted the program development stream and the DOOD 2000 conference constituted the database systems stream. Each stream had its own chair and program committee, which autonomously selected the papers in the area of the stream. Overall, 176 papers were submitted, of which 86 were selected to be presented at the conference and appear in these proceedings. The acceptance rate was uniform across the streams. In addition, LOPSTR 2000 accepted about 15 extended abstracts to be presented at the conference in the program development stream.

Algorithmic Aspects of VLSI Layout - M Sarrafzadeh 1993-11-22
In the past two decades, research in VLSI physical design has been directed toward automation of layout process. Since the cost of fabricating a circuit is a fast growing function of the circuit area, circuit layout techniques are developed with an aim to produce layouts with small areas. Other criteria of optimality such as delay and via minimization need to be taken into consideration. This book includes 14 articles that deal with various stages of the VLSI layout problem. It covers topics including partitioning, floorplanning, placement, global routing, detailed routing and layout verification. Some of the chapters are review articles, giving the state-of-the-art of the problems related to timing driven placement, global and detailed routing, and circuit partitioning. The rest of the book contains research articles, giving recent findings of new approaches to the above-mentioned problems. They are all written by leading experts in the field. This book will serve as good references for both researchers and professionals who work in this field.
Contents:Issues in Timing Driven Layout (M Marek-Sadowska)Binary Formulations for Placement and Routing Problems (S M Kang & M Sriram)A Survey of Parallel Algorithms for VLSI Cell Placement (P Banerjee)Approximate Solutions for Graph and Hypergraph Partitioning (F Makedon & S Tragoudas)Integer Program Formulations of Global Routing and Placement Problems (T Lengauer and M Lügering)Circuit Partitioning Algorithms Based on Geometry Model (T Asano & T Tokuyama)The Three-Dimensional Channel Routing Problem (M L Brady et al.)On the Manhattan and Knock-Knee Routing Models (D Zhou & F P Preparata)Switch-Box Routing Under the Two-Overlap Wiring Model (T F Gonzalez et al.)A Note on the Complexity of Stockmeyer's Floorplan Optimization Technique (T-C Wang &

D F Wong)An Algorithm to Eliminate All Complex Triangles in a Maximal Planar Graph for Use in VLSI Floorplan (S Tsukiyama et al.)Constrained Via Minimization and Signed Hypergraph Partitioning (C-J Shi)The Virtual Dimensions of a Straight Line Embedding of a Plane Graph (T Takahashi & Y Kajitani)Routing Around Two Rectangles to Minimize the Layout Area (T F Gonzalez & S L Lee) Readership: Computer scientists. keywords:

CAD/CAM Abstracts - 1992

Vlsi Cad - Chiplunkar Niranjan N.

Electronic Systems and Applications - R. P Agarwal 1994

Electronics Computer Aided Design - Phil L. Jones 1989

VLSI Circuit Layout - Te Chiang Hu 1985

Evolutionary Algorithms for VLSI CAD - Rolf Drechsler 1998-05-31

In VLSI CAD, difficult optimization problems have to be solved on a constant basis. Various optimization techniques have been proposed in the past. While some of these methods have been shown to work well in applications and have become somewhat established over the years, other techniques have been ignored. Recently, there has been a growing interest in optimization algorithms based on principles observed in nature, termed Evolutionary Algorithms (EAs). Evolutionary Algorithms in VLSI CAD presents the basic concepts of EAs, and considers the application of EAs in VLSI CAD. It is the first book to show how EAs could be used to improve IC design tools and processes. Several successful applications from different areas of circuit design, like logic synthesis, mapping and testing, are described in detail. Evolutionary Algorithms in VLSI CAD consists of two parts. The first part discusses basic principles of EAs and provides some easy-to-understand examples. Furthermore, a theoretical model for multi-objective optimization is presented. In the second part a software implementation of EAs is supplied together with detailed descriptions of several EA applications. These applications cover a wide range of VLSI CAD, and different methods for using EAs are described. Evolutionary Algorithms in VLSI CAD is intended for CAD developers and researchers as well as those working in evolutionary algorithms and techniques supporting modern design tools and processes.

Proceedings, ... International Symposium on VLSI Design - 1995

Modelling and Simulation 1992 - John Stephenson 1992

IEEE Circuits & Devices - 1994

Parallel Processing and Applications - E. Chiricozzi 1988

Tutorial--VLSI Testing & Validation Techniques - Hassan K. Reghbati 1985

Database and Expert Systems Applications - Dimitris Karagiannis 2013-11-11

The Database and Expert Systems Applications - DEXA - conferences are dedi cated to providing an international forum for the presentation of applications in the database and expert systems field, for the exchange of ideas and experiences, and for defining requirements for the future systems in these fields. After the very promising DEXA 90 in Vienna, Austria, we hope to have successfully established wjth this year's DEXA 91 a stage where scientists from diverse fields interested in application-oriented research can present and discuss their work. This year there was a total of more than 250 submitted papers from 28 different countries, in all continents. Only 98 of the papers could be accepted. The collection of papers in these proceedings offers a cross-section of the issues facing the area of databases and expert systems, i.e., topics of basic research interest on one hand and questions occurring when developing applications on the other. Major credit for the success of the conference goes to all of our colleagues who submitted papers for consideration and to those who have organized and chaired the panel sessions. Many persons contributed numerous hours to organize this conference. The names of most of them will appear on the following pages. In particular we

wish to thank the Organization Committee Chairmen Johann Gordesch, A Min Tjoa, and Roland Wag ner, who also helped establishing the program. Special thanks also go to Gabriella Wagner and Anke Ruckert. Dimitris Karagiannis General Conference Chairman Contents Conference Committee.